

Figure 1 (Prior Art)

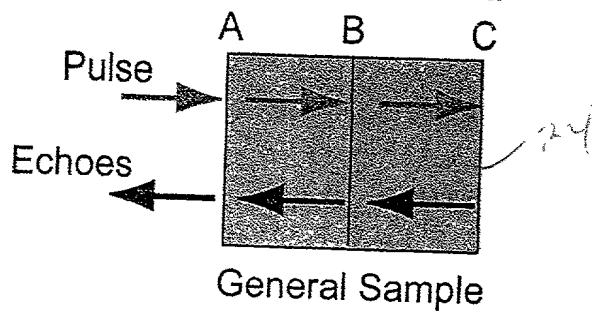


Figure 1A (Prior Art)

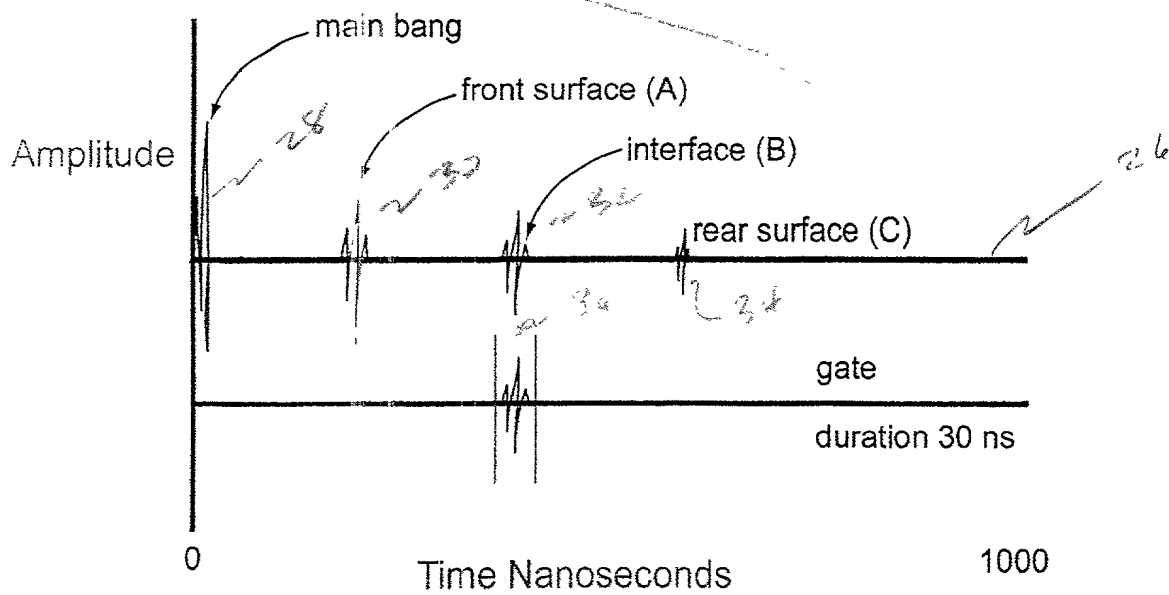


Figure 1B (Prior Art)

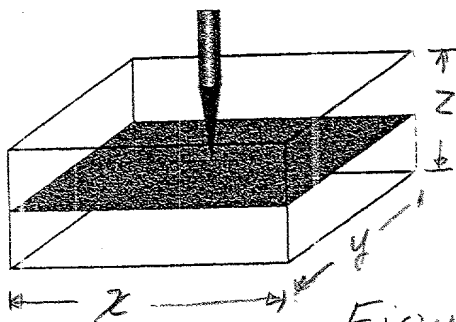


Figure 2 (Prior Art)

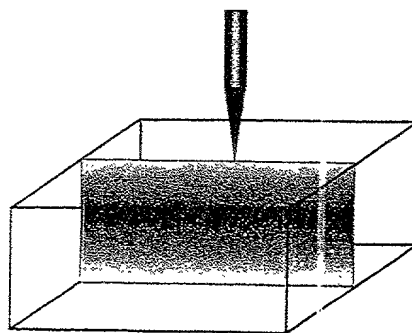


Figure 3 (Prior Art)

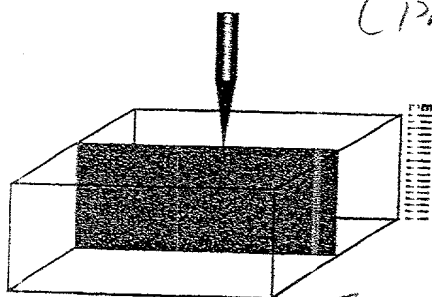


Figure 4 (Prior Art)

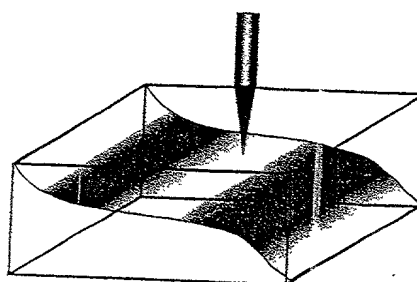


Figure 5 (Prior Art)

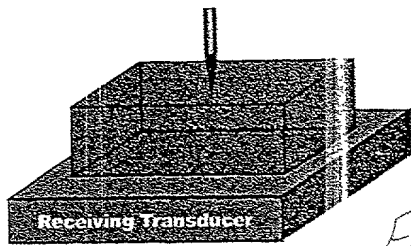


Figure 4
(Prior Art)

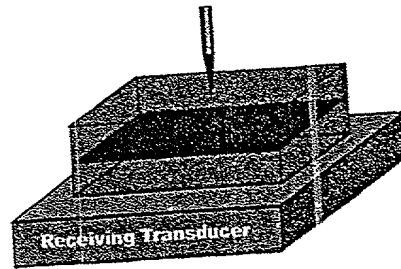


Figure 7
(Prior Art)

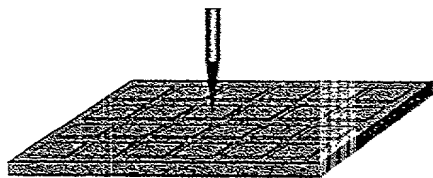


Figure 8
(Prior Art)

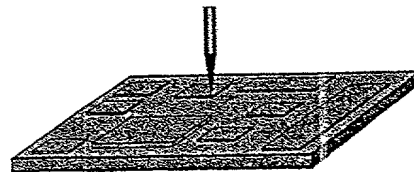


Figure 9
(Prior Art)

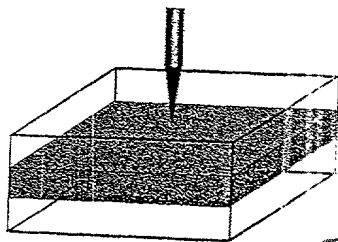


Figure 10
(Prior Art)

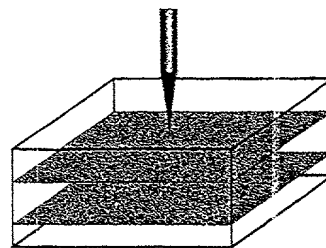


Figure 11
(Prior Art)

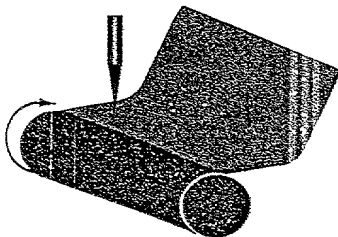


Figure 12
(Prior Art)

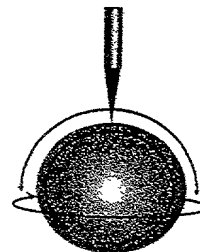


Figure 13
(Prior Art)

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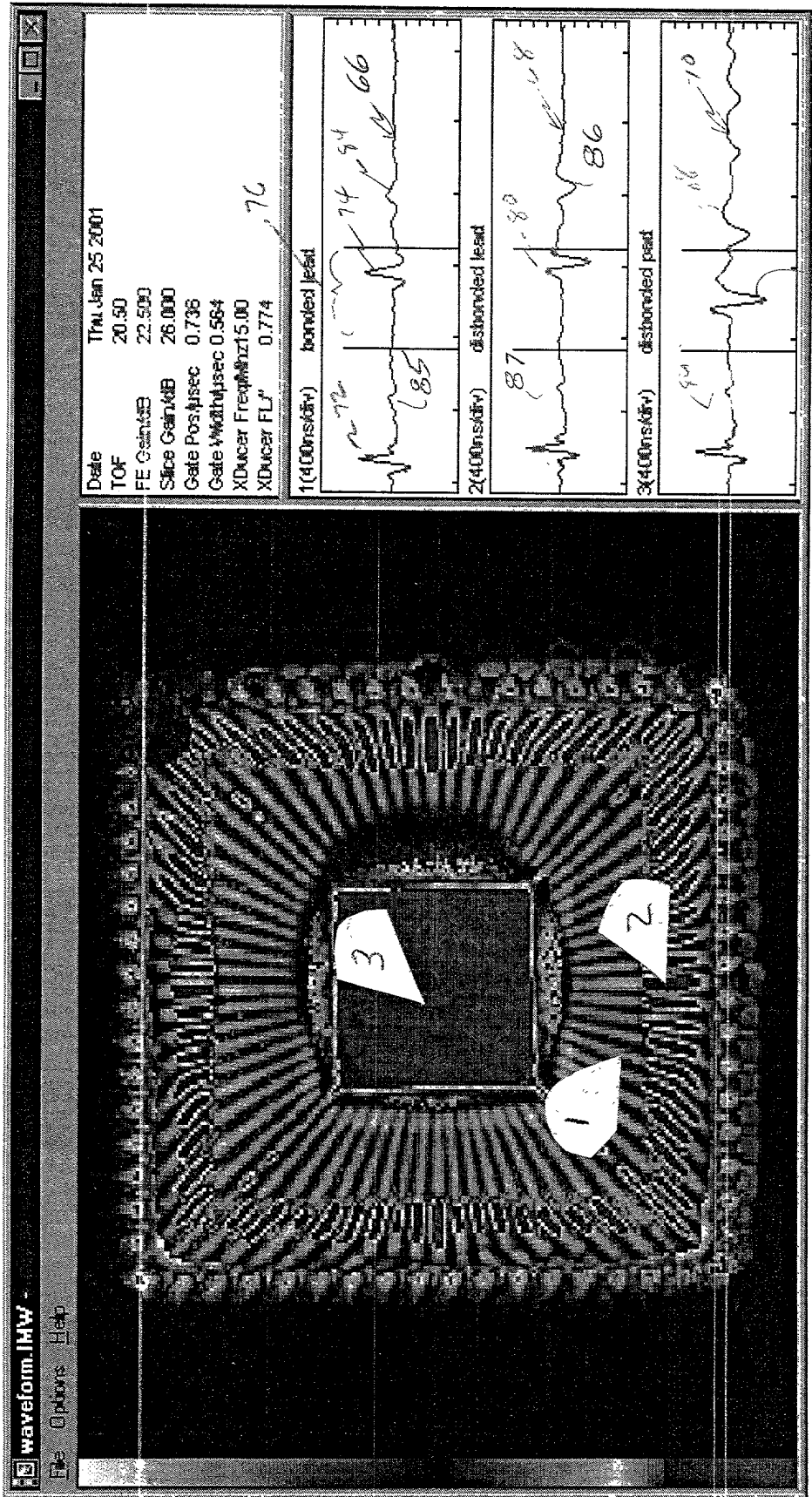
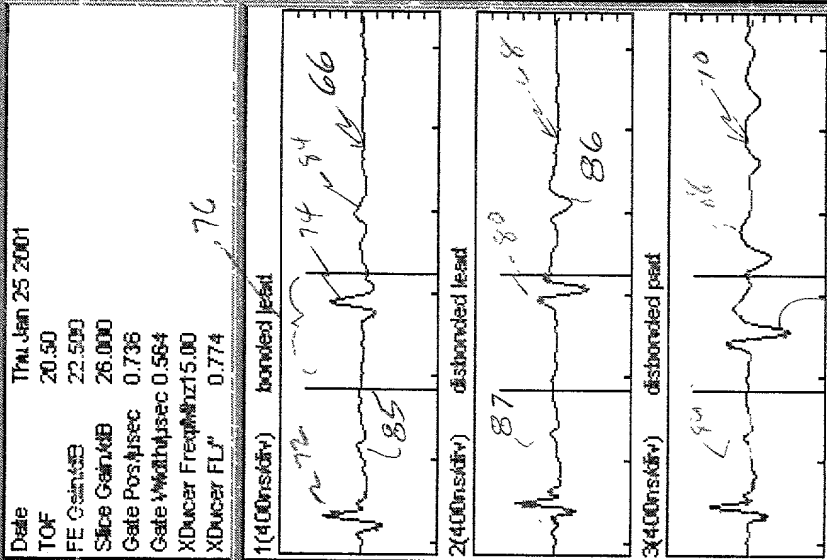


Figure 14 (Prior Art)



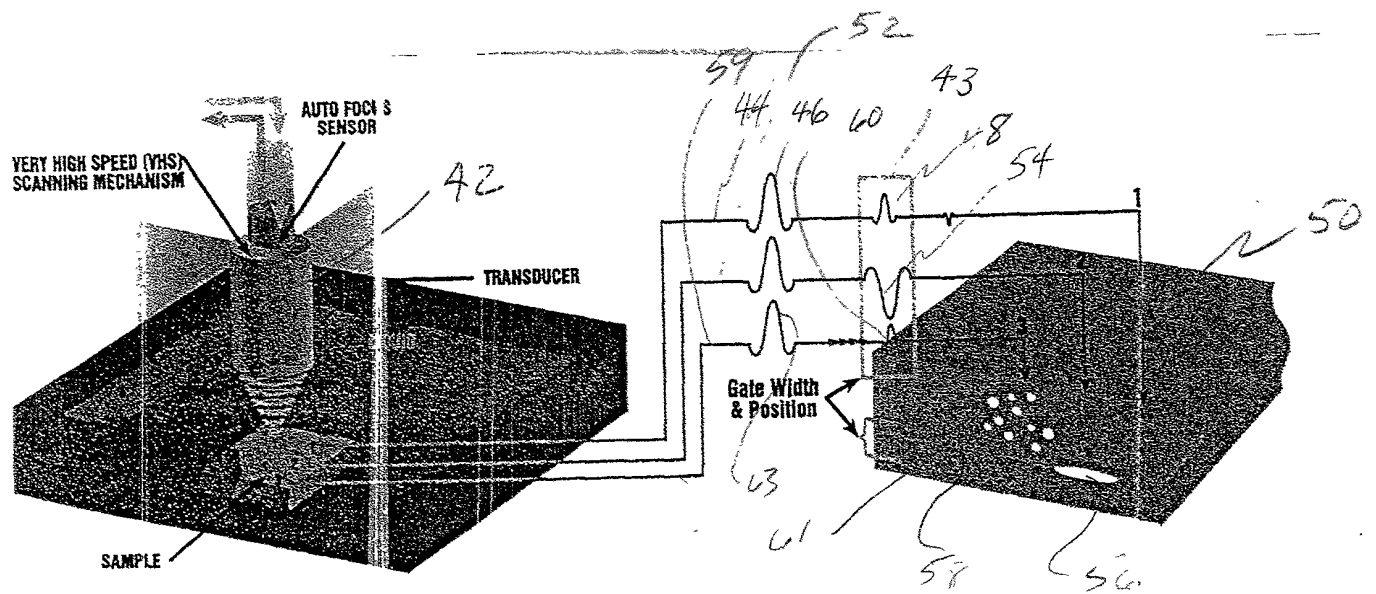


Figure 2A (Prior Art)

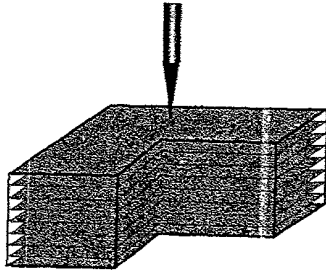


Figure 15
(Prior Art)

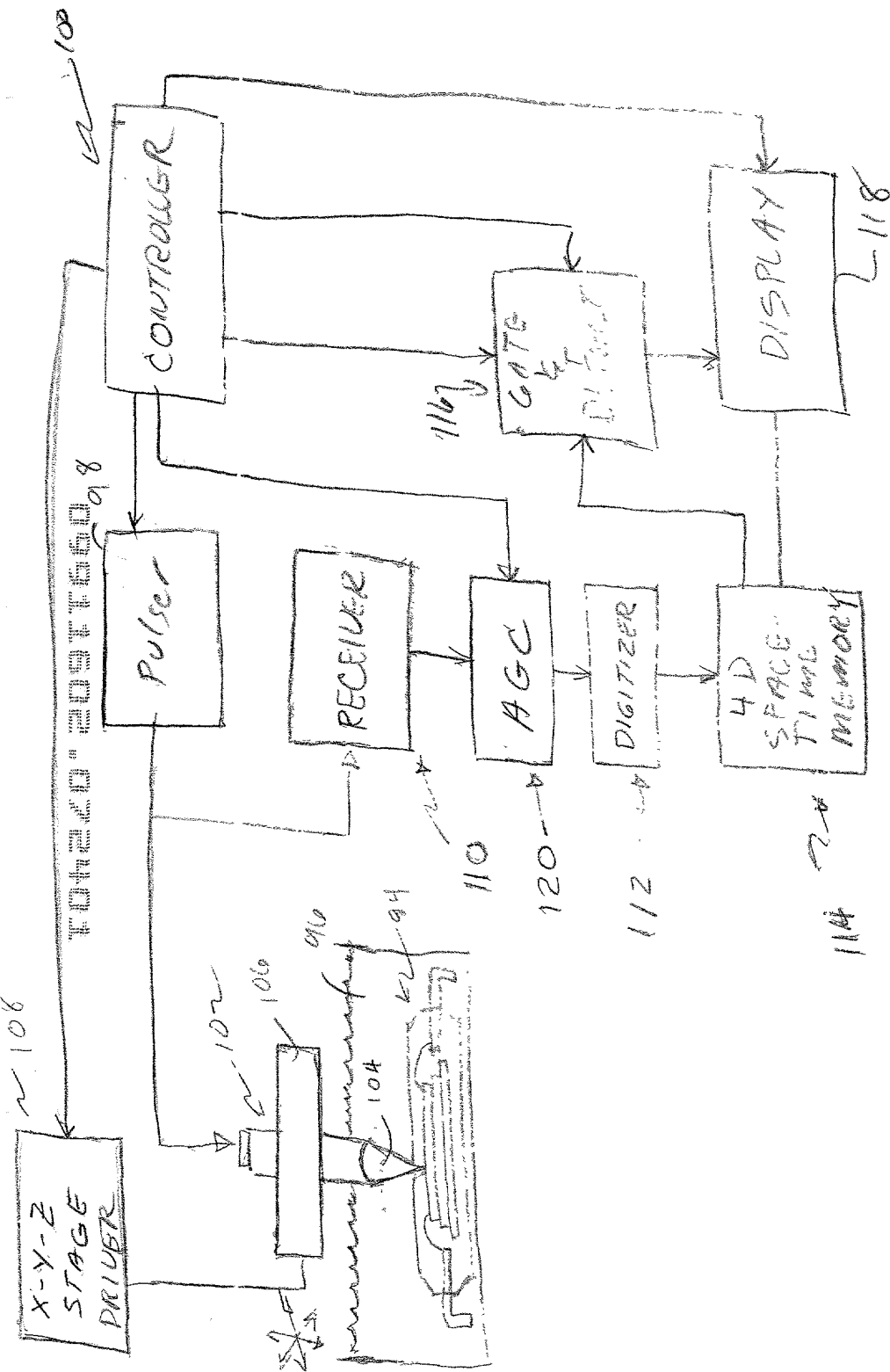


Figure 16

[illegible]

Figure 16A

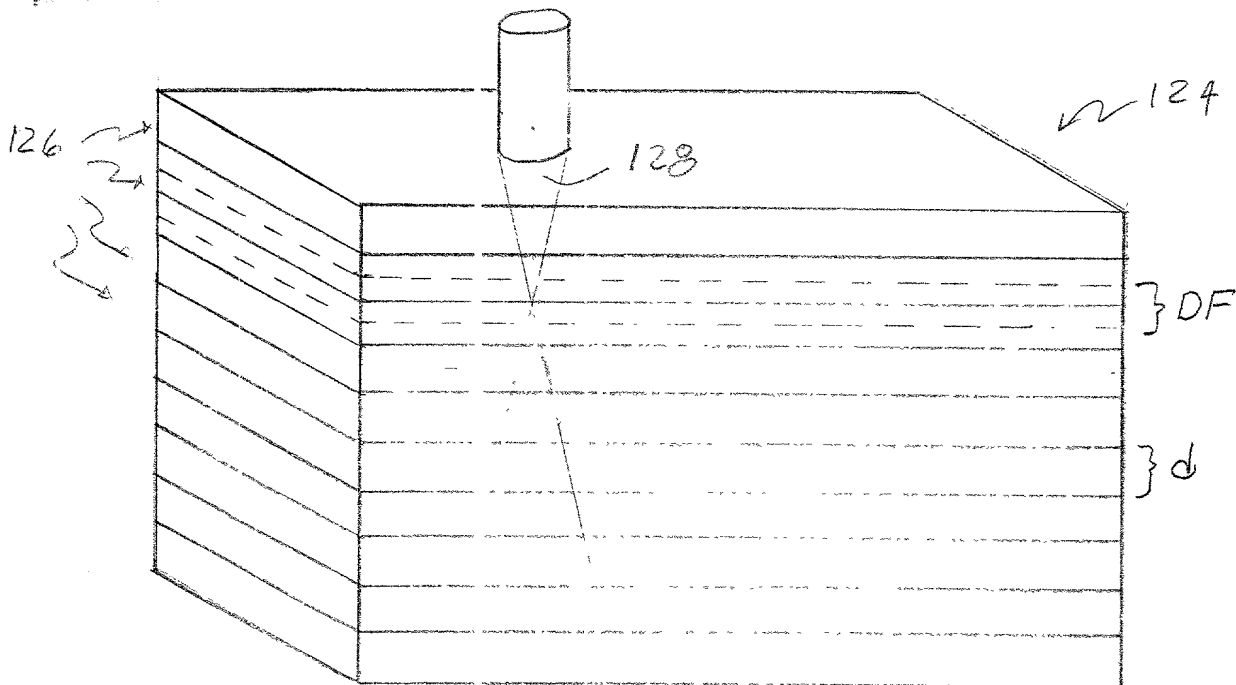


Figure 17

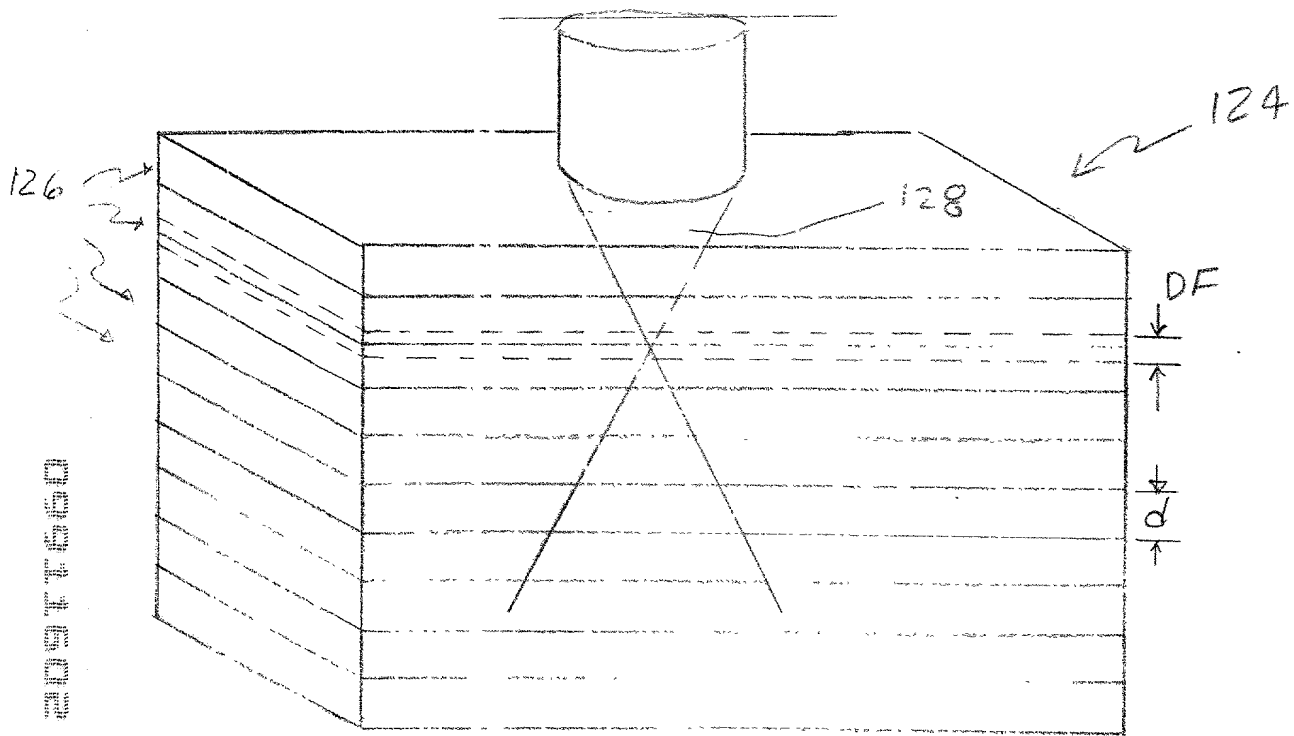


Figure 18

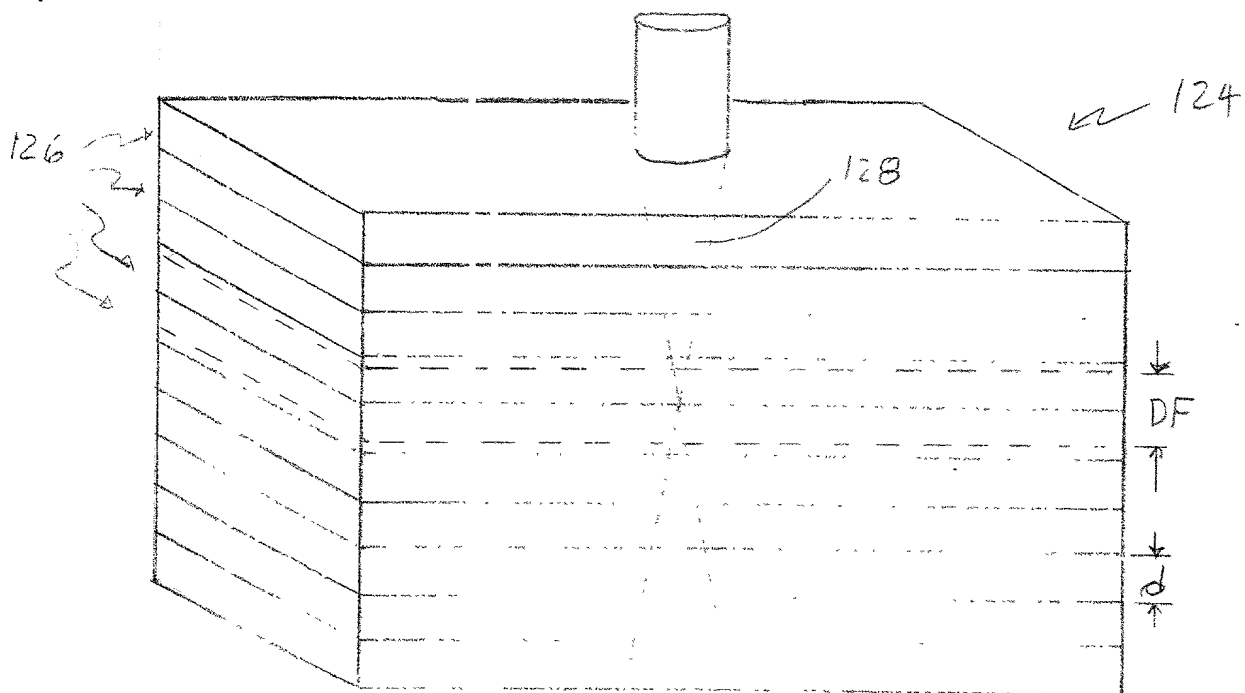


Figure 19

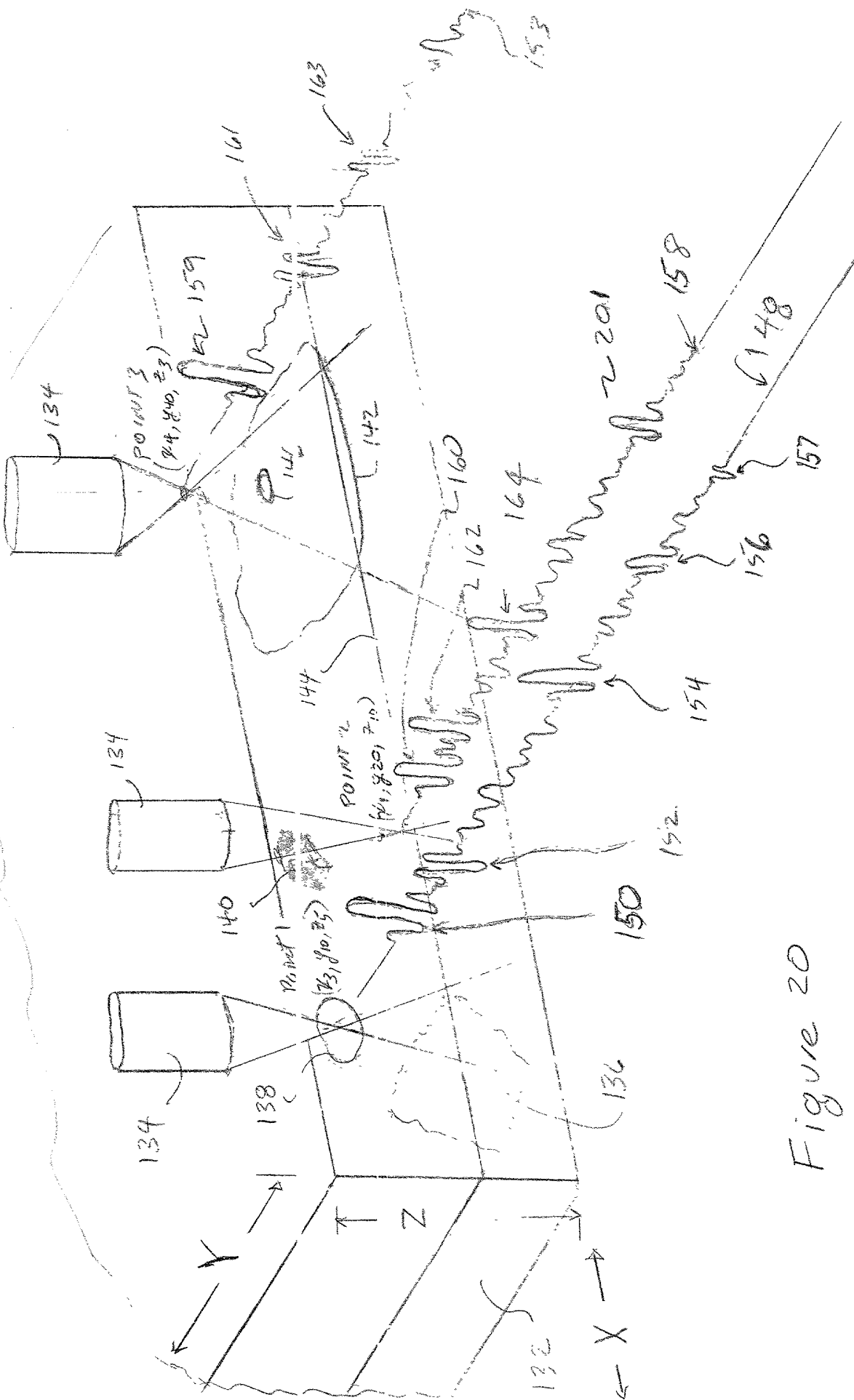


Figure 20

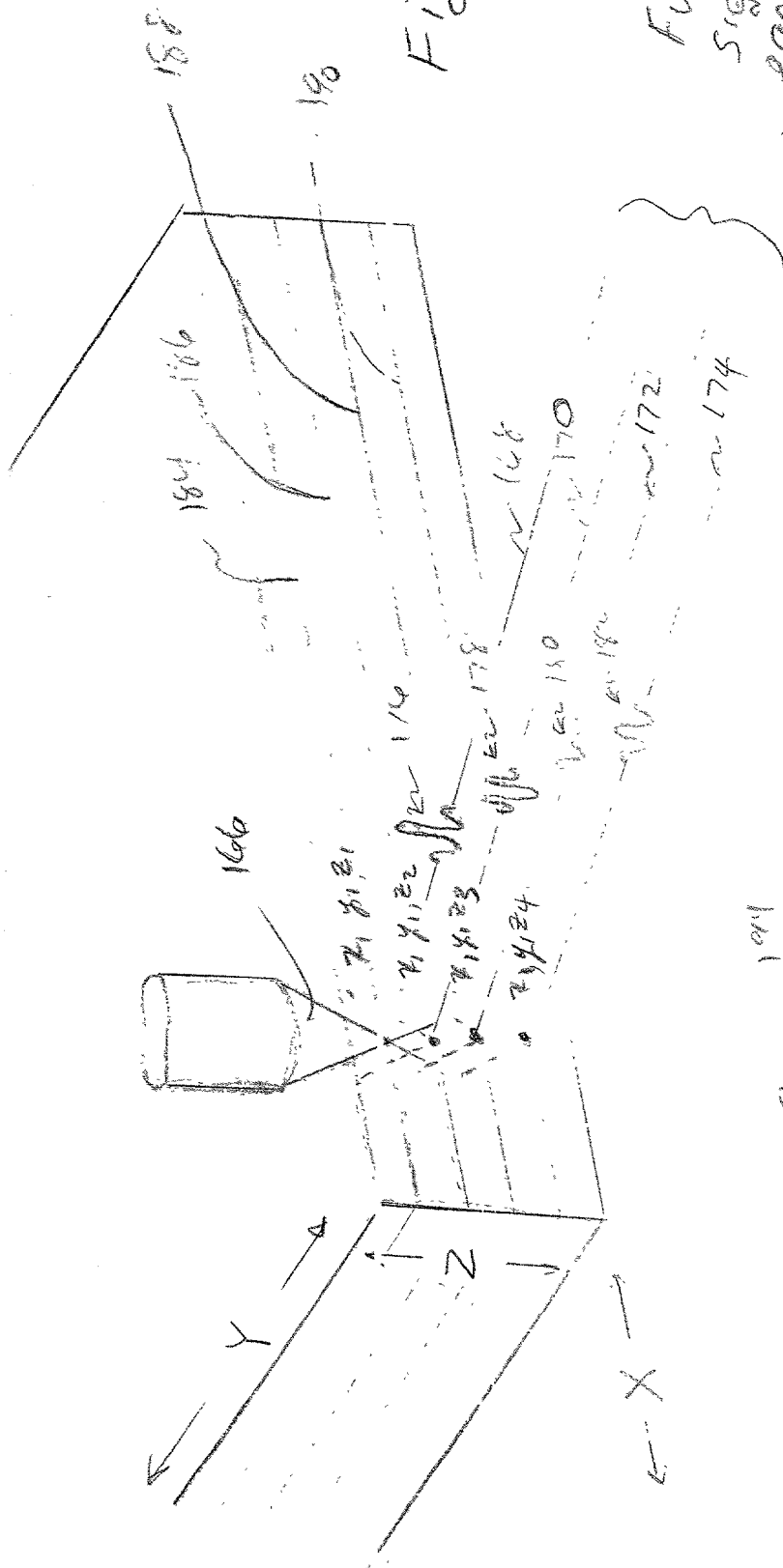


Figure 21

Full echo
Signal for
2000
& stored in 4D
memory

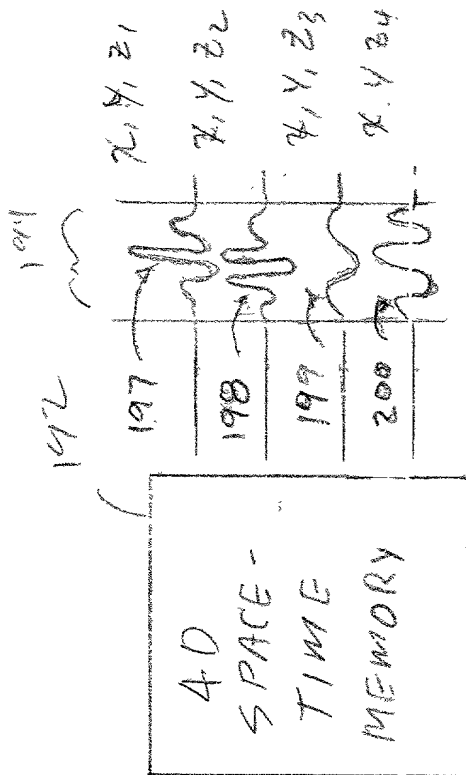


Figure 22

$x, y, z_1, x, y, z_2, x, y, z_3, x, y, z_4$
197 198 199 200

Full echo
Signal for
2000
& stored in 4D
memory

Figure 23